

Day : Monday
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PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = TAN

First Name = KEN-SUE

Application#	Patent#	Status	Date Filed	Title	Inventor Name 4
<u>09954663</u>	Not Issued	030	09/12/2001	METHOD AND APPARATUS FOR SENDING AUXILIARY DATA ON A TMDS-LIKE LINK	TAN, KEN-SUE
<u>09954291</u>	Not Issued	030	09/12/2001	METHOD AND APPARATUS FOR SYNCHRONIZING AUXILIARY DATA AND VIDEO DATA TRANSMITTED OVER A TMDS-LIKE LINK	TAN, KEN-SUE
<u>09953703</u>	Not Issued	030	09/12/2001	METHOD AND APPARATUS FOR ENCRYPTING AUXILIARY DATA TRANSMITTED OVER A TMDS-LIKE LINK	TAN, KEN-SUE
<u>09298647</u>	6259427	150	04/23/1999	SCALING MULTI-DIMENSIONAL SIGNALS USING VARIABLE WEIGHTING FACTORS	TAN , KEN-SUE

Inventor Search Completed: No Records to Display.

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Day : Monday
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PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = KIM

First Name = MIN-KYU

Application#	Patent#	Status	Date Filed	Title	Inventor Name 24
<u>60296655</u>	Not Issued	159	06/06/2001	METHOD AND APPARATUS FOR ADAPTIVE CONTROL OF PLL LOOP BANDWIDTH	KIM, MIN-KYU
<u>60296654</u>	Not Issued	159	06/06/2001	PSEUDO PIXEL ERROR DETECTOR FOR TMDS	KIM, MIN-KYU
<u>10915221</u>	Not Issued	030	08/10/2004	MEMORY CONTROLLER THAT SELECTIVELY CHANGES FREQUENCY OF A MEMORY CLOCK SIGNAL, A SMART CARD INCLUDING THE SAME, AND A METHOD OF CONTROLLING A READ OPERATION OF A MEMORY	KIM, MIN-KYU
<u>10835301</u>	Not Issued	071	04/29/2004	METHOD AND APPARATUS FOR RUN LENGTH LIMITED TMDS-LIKE ENCODING OF DATA	KIM, MIN-KYU
<u>10473259</u>	Not Issued	020	09/29/2003	AZIRIDINE DERIVATIVES AND THEIR PREPARATION METHODS	KIM, MIN-KYU
<u>10371220</u>	Not Issued	030	02/19/2003	DATA SYNCHRONIZATION ACROSS AN ASYNCHRONOUS BOUNDARY USING, FOR EXAMPLE, MULTI-PHASE CLOCKS	KIM, MIN-KYU
<u>10315733</u>	6741321	150	12/09/2002	RAW PANEL FOR LIQUID CRYSTAL DISPLAY AND MANUFACTURING METHOD FOR COLOR LIQUID CRYSTAL DISPLAY USING RAW PANEL	KIM, MIN-KYU
<u>10247551</u>	Not Issued	041	09/20/2002	METHOD OF DRIVING CHOLESTERIC LIQUID CRYSTAL DISPLAY PANEL FOR ACCURATE GRAY-SCALE	KIM, MIN-KYU

				DISPLAY	
<u>10099533</u>	Not Issued	030	03/15/2002	COMBINING A CLOCK SIGNAL AND A DATA SIGNAL	KIM, MIN-KYU
<u>10024299</u>	6693827	150	12/21/2001	MEMORY CELL SENSING CIRCUIT	KIM, MIN-KYU
<u>10018333</u>	Not Issued	041	02/15/2002	APPARATUS AND METHOD FOR INPUTTING ALPHABET CHARACTERS ON SMALL KEYPAD	KIM, MIN-KYUM MIN
<u>09989647</u>	Not Issued	030	11/20/2001	LOGIC GATES INCLUDING DIODE-CONNECTED METAL-OXIDE-SEMICONDUCTOR FIELD-EFFECT TRANSISTORS (MOSFETS) TO CONTROL INPUT THRESHOLD VOLTAGE LEVELS AND SWITCHING TRANSIENTS OF OUTPUT LOGIC SIGNALS	KIM, MIN-KYU
<u>09989580</u>	Not Issued	030	11/20/2001	BIDIRECTIONAL BRIDGE CIRCUIT HAVING HIGH COMMON MODE REJECTION AND HIGH INPUT SENSITIVITY	KIM, MIN-KYU
<u>09989487</u>	Not Issued	020	11/20/2001	DIFFERENTIAL AMPLIFIERS USING ASYMMETRIC TRANSFER CHARACTERISTICS TO SUPPRESS INPUT NOISE IN OUTPUT LOGIC SIGNALS	KIM, MIN-KYU
<u>09954663</u>	Not Issued	030	09/12/2001	METHOD AND APPARATUS FOR SENDING AUXILIARY DATA ON A TMDS-LIKE LINK	KIM, MIN-KYU
<u>09954291</u>	Not Issued	030	09/12/2001	METHOD AND APPARATUS FOR SYNCHRONIZING AUXILIARY DATA AND VIDEO DATA TRANSMITTED OVER A TMDS-LIKE LINK	KIM, MIN-KYU
<u>09953703</u>	Not Issued	030	09/12/2001	METHOD AND APPARATUS FOR ENCRYPTING AUXILIARY DATA TRANSMITTED OVER A TMDS-LIKE LINK	KIM, MIN-KYU
<u>09905615</u>	Not Issued	041	07/13/2001	SYSTEM AND METHOD FOR MEASURING PSEUDO PIXEL ERROR RATE	KIM, MIN-KYU
<u>09905511</u>	Not Issued	041	07/13/2001	METHOD AND APPARATUS FOR ADAPTIVE CONTROL OF	KIM, MIN-KYU

				PLL LOOP BANDWIDTH	
<u>09881271</u>	Not Issued	041	06/14/2001	METHODS AND SYSTEMS FOR SENDING SIDE-CHANNEL DATA DURING DATA INACTIVE PERIOD	KIM, MIN-KYU
<u>09709816</u>	Not Issued	161	11/10/2000	SYSTEM AND METHOD FOR SENDING AND RECEIVING DATA SIGNALS OVER A CLOCK SIGNAL LINE	KIM, MIN-KYU
<u>09393235</u>	6463092	150	09/09/1999	SYSTEM AND METHOD FOR SENDING AND RECEIVING DATA SIGNALS OVER A CLOCK SIGNAL LINE	KIM , MIN-KYU
<u>09291745</u>	Not Issued	161	04/14/1999	METHOD OF SELECTING OPTIMUM VALUE OF INPUT VARIABLE	KIM , MIN-KYU
<u>08392344</u>	5786860	150	02/22/1995	HIGH SPEED BLOCK MATCHING FOR BI-DIRECTIONAL MOTION VECTOR ESTIMATION	KIM , MIN-KYU

Inventor Search Completed: No Records to Display.

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Last Name

kim

First Name

min-kyu

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PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = KIM

First Name = GYUDONG

Application#	Patent#	Status	Date Filed	Title	Inventor Name 42
<u>60475032</u>	Not Issued	159	06/02/2003	DATA SAMPLING METHOD AND APPARATUS USING THROUGH-TRANSITION COUNTS TO REJECT WORST SAMPLING POSITION	KIM, GYUDONG
<u>60296655</u>	Not Issued	159	06/06/2001	METHOD AND APPARATUS FOR ADAPTIVE CONTROL OF PLL LOOP BANDWIDTH	KIM, GYUDONG
<u>60296654</u>	Not Issued	159	06/06/2001	PSEUDO PIXEL ERROR DETECTOR FOR TMDS	KIM, GYUDONG
<u>60296653</u>	Not Issued	159	06/06/2001	CLOCK DIVIDER SCHEME FOR MULTIPLE-PHASE CLOCK GENERATION	KIM, GYUDONG
<u>60282672</u>	Not Issued	159	04/09/2001	MULTI-PHASE VCO WITH COMMON MODE CONTROL	KIM, GYUDONG
<u>60277219</u>	Not Issued	159	03/19/2001	ENCODING SCHEME FOR A PHASE MODULATED CLOCK SIGNAL	KIM, GYUDONG
<u>60276672</u>	Not Issued	159	03/16/2001	ENCODING SCHEME FOR A PHASE MODULATED CLOCK SIGNAL	KIM, GYUDONG
<u>60222859</u>	Not Issued	159	08/04/2000	SCALABLE DIGITAL DISPLAY CONTROLLER DEJITTER CIRCUIT	KIM, GYUDONG
<u>60136695</u>	Not Issued	159	05/28/1999	ENCRYPTION METHOD FOR TMDS	KIM , GYUDONG
<u>60117488</u>	Not Issued	159	01/27/1999	ENCRYPTION METHOD FOR TMDS	KIM , GYUDONG
<u>60099770</u>	Not Issued	159	09/10/1998	EMBEDDED BACK CHANNEL FOR TMDS	KIM , GYUDONG
<u>60071879</u>	Not Issued	159	01/20/1998	1.25GBAUD CMOS DRIVER AND ON-CHIP TERMINATION FOR GIGABIT ETHERNET PHY	KIM , GYUDONG

				CHIP	
<u>60071805</u>	Not Issued	159	01/20/1998	SUPPRESSION OF ELECTROMAGNETIC INTERFERENCE IN PARALLEL DATA CHANNELS THROUGH SPREAD SPECTRUM PHASE MODULATION	KIM , GYUDONG
<u>60058042</u>	Not Issued	159	09/04/1997	POWER SAVING METHODS FOR COLUMN DRIVERS	KIM , GYUDONG
<u>60058041</u>	Not Issued	159	09/04/1997	DATA DRIVING WITH REDUCED EMI	KIM , GYUDONG
<u>10842231</u>	Not Issued	020	05/10/2004	DATA SAMPLING METHOD AND APPARATUS USING THROUGH-TRANSITION COUNTS TO REJECT WORST SAMPLING POSITION	KIM, GYUDONG
<u>10835301</u>	Not Issued	071	04/29/2004	METHOD AND APPARATUS FOR RUN LENGTH LIMITED TMDS-LIKE ENCODING OF DATA	KIM, GYUDONG
<u>10794015</u>	Not Issued	020	03/05/2004	METHOD AND CIRCUIT FOR ADAPTIVE EQUALIZATION OF MULTIPLE SIGNALS IN RESPONSE TO A CONTROL SIGNAL GENERATED FROM ONE OF THE EQUALIZED SIGNALS	KIM, GYUDONG
<u>10781405</u>	Not Issued	020	02/18/2004	CABLE WITH CIRCUITRY FOR ASSERTING STORED CABLE DATA OR OTHER INFORMATION TO AN EXTERNAL DEVICE OR USER	KIM, GYUDONG
<u>10763905</u>	Not Issued	030	01/23/2004	REDUCED DEAD-CYCLE, ADAPTIVE PHASE TRACKING METHOD AND APPARATUS	KIM, GYUDONG
<u>10439446</u>	Not Issued	071	05/16/2003	METHOD AND CIRCUIT FOR DC MEASUREMENT OF PHASE UNIFORMITY OF MULTI-PHASE CLOCKS	KIM, GYUDONG
<u>10371220</u>	Not Issued	030	02/19/2003	DATA SYNCHRONIZATION ACROSS AN ASYNCHRONOUS BOUNDARY USING, FOR EXAMPLE, MULTI-PHASE CLOCKS	KIM, GYUDONG
<u>10099533</u>	Not	030	03/15/2002	COMBINING A CLOCK	KIM, GYUDONG

	Issued			SIGNAL AND A DATA SIGNAL	
<u>09989647</u>	Not Issued	030	11/20/2001	LOGIC GATES INCLUDING DIODE-CONNECTED METAL-OXIDE-SEMICONDUCTOR FIELD-EFFECT TRANSISTORS (MOSFETS) TO CONTROL INPUT THRESHOLD VOLTAGE LEVELS AND SWITCHING TRANSIENTS OF OUTPUT LOGIC SIGNALS	KIM, GYUDONG
<u>09989645</u>	6809567	150	11/20/2001	SYSTEM AND METHOD FOR MULTIPLE-PHASE CLOCK GENERATION	KIM, GYUDONG
<u>09989587</u>	6717478	150	11/20/2001	MULTI-PHASE VOLTAGE CONTROLLED OSCILLATOR (VCO) WITH COMMON MODE CONTROL	KIM, GYUDONG
<u>09989580</u>	Not Issued	030	11/20/2001	BIDIRECTIONAL BRIDGE CIRCUIT HAVING HIGH COMMON MODE REJECTION AND HIGH INPUT SENSITIVITY	KIM, GYUDONG
<u>09989487</u>	Not Issued	020	11/20/2001	DIFFERENTIAL AMPLIFIERS USING ASYMMETRIC TRANSFER CHARACTERISTICS TO SUPPRESS INPUT NOISE IN OUTPUT LOGIC SIGNALS	KIM, GYUDONG
<u>09954663</u>	Not Issued	030	09/12/2001	METHOD AND APPARATUS FOR SENDING AUXILIARY DATA ON A TMDS-LIKE LINK	KIM, GYUDONG
<u>09954291</u>	Not Issued	030	09/12/2001	METHOD AND APPARATUS FOR SYNCHRONIZING AUXILIARY DATA AND VIDEO DATA TRANSMITTED OVER A TMDS-LIKE LINK	KIM, GYUDONG
<u>09953703</u>	Not Issued	030	09/12/2001	METHOD AND APPARATUS FOR ENCRYPTING AUXILIARY DATA TRANSMITTED OVER A TMDS-LIKE LINK	KIM, GYUDONG
<u>09922990</u>	Not Issued	041	08/03/2001	DIGITAL DISPLAY JITTER CORRECTION APPARATUS AND METHOD	KIM, GYUDONG
<u>09905615</u>	Not	041	07/13/2001	SYSTEM AND METHOD FOR	KIM, GYUDONG

	Issued			MEASURING PSEUDO PIXEL ERROR RATE	
<u>09905511</u>	Not Issued	041	07/13/2001	METHOD AND APPARATUS FOR ADAPTIVE CONTROL OF PLL LOOP BANDWIDTH	KIM, GYUDONG
<u>09881271</u>	Not Issued	041	06/14/2001	METHODS AND SYSTEMS FOR SENDING SIDE-CHANNEL DATA DURING DATA INACTIVE PERIOD	KIM, GYUDONG
<u>09709816</u>	Not Issued	161	11/10/2000	SYSTEM AND METHOD FOR SENDING AND RECEIVING DATA SIGNALS OVER A CLOCK SIGNAL LINE	KIM, GYUDONG
<u>09579811</u>	Not Issued	092	05/26/2000	METHODS AND SYSTEMS FOR TMDS ENCRYPTION	KIM, GYUDONG
<u>09393235</u>	6463092	150	09/09/1999	SYSTEM AND METHOD FOR SENDING AND RECEIVING DATA SIGNALS OVER A CLOCK SIGNAL LINE	KIM , GYUDONG
<u>09234777</u>	6600771	150	01/20/1999	SPREAD SPECTRUM PHASE MODULATION FOR SUPPRESSION OF ELECTROMAGNETIC INTERFERENCE IN PARALLEL DATA CHANNELS	KIM , GYUDONG
<u>09148815</u>	6144242	150	09/04/1998	CONTROLLABLE DELAYS IN MULTIPLE SYNCHRONIZED SIGNALS FOR REDUCED ELECTROMAGNETIC INTERFERENCE AT PEAK FREQUENCIES	KIM , GYUDONG
<u>09148583</u>	6271816	150	09/04/1998	POWER SAVING CIRCUIT AND METHOD FOR DRIVING AN ACTIVE MATRIX DISPLAY	KIM , GYUDONG
<u>08815486</u>	6157360	150	03/11/1997	SYSTEM AND METHOD FOR DRIVING COLUMNS OF AN ACTIVE MATRIX DISPLAY	KIM , GYUDONG

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Last Name

First Name

kim

gyudong

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PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = MARTIN

First Name = RUSSEL A.

Application#	Patent#	Status	Date Filed	Title	Inventor Name 16
<u>60160494</u>	Not Issued	159	10/20/1999	DIGITAL SYSTEM AND METHOD FOR SCALING IMAGES TO BE SHOWN ON CATHODE RAY TUBES	MARTIN , RUSSEL A.
<u>60099826</u>	Not Issued	159	09/10/1998	BIDIRECTIONAL DATA TRANSFER USING TWO PAIR OF DIFFERENTIAL LINE AS A SINGLE ADDITIONAL DIFFERENTIAL PAIR IN A DIGITAL DATA STREAM	MARTIN , RUSSEL A.
<u>60099821</u>	Not Issued	159	09/10/1998	BIDIRECTIONAL DATA TRANSFER USING THE VIDEO BLANKING PERIOD IN A DIGITAL DATA STREAM	MARTIN , RUSSEL A.
<u>60083051</u>	Not Issued	159	04/24/1998	VARIABLE WEIGHTING METHOD FOR CHANGING THE SAMPLING RATE OF A SIGNAL OR IMAGE	MARTIN , RUSSEL A.
<u>09393234</u>	<u>6307543</u>	150	09/09/1999	BI-DIRECTIONAL DATA TRANSFER USING TWO PAIR OF DIFFERENTIAL LINES AS A SINGLE ADDITIONAL DIFFERENTIAL PAIR	MARTIN , RUSSEL A.
<u>09298647</u>	<u>6259427</u>	150	04/23/1999	SCALING MULTI-DIMENSIONAL SIGNALS USING VARIABLE WEIGHTING FACTORS	MARTIN , RUSSEL A.
<u>08679169</u>	<u>6078936</u>	150	07/12/1996	PRESENTING AN IMAGE ON A DISPLAY AS IT WOULD BE PRESENTED BY ANOTHER IMAGE OUTPUT DEVICE OR ON PRINTING CIRCUITRY	MARTIN , RUSSEL A.
<u>08679168</u>	<u>5703621</u>	150	07/12/1996	UNIVERSAL DISPLAY THAT PRESENTS ALL IMAGE	MARTIN , RUSSEL A.

				TYPES WITH HIGH IMAGE FIDELITY	
<u>08579048</u>	<u>5731803</u>	150	12/21/1995	ARRAY WITH LIGHT ACTIVE UNITS SIZED TO ELIMINATE ARTIFACT FROM SIZE DIFFERENCE	MARTIN , RUSSEL A.
<u>08576183</u>	<u>5608245</u>	150	12/21/1995	ARRAY ON SUBSTRATE WITH REPAIR LINE CROSSING LINES IN THE ARRAY	MARTIN , RUSSEL A.
<u>08301952</u>	<u>5644349</u>	150	09/07/1994	MECHANICAL CAPACITOR	MARTIN , RUSSEL A.
<u>08235017</u>	Not Issued	166	04/28/1994	PRESENTING AN IMAGE ON A DISPLAY AS IT WOULD BE PRESENTED BY ANOTHER IMAGE OUTPUT DEVICE OR ON PRINTING CIRCUITRY	MARTIN , RUSSEL A.
<u>08235015</u>	Not Issued	166	04/28/1994	UNIVERSAL DISPLAY THAT PRESENTS ALL IMAGE TYPES WITH HIGH IMAGE FIDELITY	MARTIN , RUSSEL A.
<u>08235011</u>	<u>5491347</u>	150	04/28/1994	THIN-FILM STRUCTURE WITH DENSE ARRAY OF BINARY CONTROL UNITS FOR PRESENTING IMAGES	MARTIN , RUSSEL A.
<u>06818384</u>	<u>4680605</u>	150	01/13/1986	HIGH VOLTAGE DEPLETION MODE TRANSISTOR WITH SERPENTINE CURRENT PATH	MARTIN , RUSSEL A.
<u>06588172</u>	Not Issued	166	03/12/1984	HIGH VOLTAGE DEPLETION MODE TRANSISTOR WITH SERPENTINE CURRENT PATH	MARTIN , RUSSEL A.

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PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = LYLE

First Name = JAMES

Application#	Patent#	Status	Date Filed	Title	Inventor Name 21
<u>60439903</u>	Not Issued	159	01/13/2003	METHOD AND APPARATUS FOR CONTENT PROTECTION WITHIN AN OPEN ARCHITECTURE SYSTEM	LYLE, JAMES D.
<u>60326696</u>	Not Issued	159	10/01/2001	CRYPTOSYSTEM	LYLE, JAMES
<u>10968741</u>	Not Issued	019	10/19/2004	METHOD AND APPARATUS FOR CONTENT PROTECTION IN A PERSONAL DIGITAL NETWORK ENVIRONMENT	LYLE, JAMES D.
<u>10679055</u>	Not Issued	020	10/03/2003	METHOD AND APPARATUS FOR CONTENT PROTECTION WITHIN AN OPEN ARCHITECTURE SYSTEM	LYLE, JAMES D.
<u>10298722</u>	Not Issued	030	11/18/2002	INTEGRATED CIRCUIT HAVING SELF TEST CAPABILITY USING MESSAGE DIGEST AND METHOD FOR TESTING INTEGRATED CIRCUIT HAVING MESSAGE DIGEST GENERATION CIRCUITRY	LYLE, JAMES D.
<u>10277858</u>	Not Issued	030	10/22/2002	INTEGRATED ADDRESSING SCHEME FOR USE IN A SYSTEM HAVING A TREE STRUCTURE	LYLE, JAMES D.
<u>10268832</u>	Not Issued	030	10/10/2002	CRYPTOGRAPHIC DEVICE WITH STORED KEY DATA AND METHOD FOR USING STORED KEY DATA TO PERFORM AN AUTHENTICATION EXCHANGE OR SELF TEST	LYLE, JAMES D.
<u>10261726</u>	Not	030	09/30/2002	SECURE DATA TRANSFER	LYLE, JAMES D.

	Issued					
<u>10224995</u>	Not Issued	030	08/21/2002	SYSTEM AND METHOD FOR AN ADAPTIVE STATE MACHINE TO CONTROL SIGNAL FILTERING IN A SERIAL LINK	LYLE, JAMES D.	
<u>10192296</u>	Not Issued	089	07/10/2002	METHOD AND SYSTEM FOR VIDEO AND AUXILIARY DATA TRANSMISSION OVER A SERIAL LINK	LYLE, JAMES D.	
<u>10120685</u>	Not Issued	030	04/11/2002	METHOD AND SYSTEM USING TRUSTED AGENTS FOR KEY DISTRIBUTION	LYLE, JAMES D.	
<u>10085177</u>	Not Issued	020	02/27/2002	METHOD AND SYSTEM FOR TEMPORARY INTERRUPTION OF VIDEO DATA TRANSMISSION	LYLE, JAMES D.	
<u>09991057</u>	Not Issued	030	11/21/2001	METHOD AND SYSTEM FOR ENCRYPTING AND DECRYPTING DATA USING AN EXTERNAL AGENT	LYLE, JAMES D.	
<u>09954663</u>	Not Issued	030	09/12/2001	METHOD AND APPARATUS FOR SENDING AUXILIARY DATA ON A TMDS-LIKE LINK	LYLE, JAMES D.	
<u>09954291</u>	Not Issued	030	09/12/2001	METHOD AND APPARATUS FOR SYNCHRONIZING AUXILIARY DATA AND VIDEO DATA TRANSMITTED OVER A TMDS-LIKE LINK	LYLE, JAMES D.	
<u>09953703</u>	Not Issued	030	09/12/2001	METHOD AND APPARATUS FOR ENCRYPTING AUXILIARY DATA TRANSMITTED OVER A TMDS-LIKE LINK	LYLE, JAMES D.	
<u>09945278</u>	Not Issued	030	08/31/2001	METHOD AND APPARATUS FOR ENCRYPTING DATA TRANSMITTED OVER A SERIAL LINK	LYLE, JAMES D.	
<u>08572070</u>	5721739	250	12/14/1995	METHOD FOR DETECTING READ ERRORS, CORRECTING SINGLE-BIT READ ERRORS AND REPORTING MULTIPLE-BIT READ ERRORS	LYLE , JAMES D.	
<u>07741251</u>	Not Issued	161	07/29/1991	LOW VOLTAGE TELEPHONE CORD ADAPTER	LYLES , JAMES H.	
<u>06531493</u>	Not	169	09/12/1983	SIMULATED GOLD OR SILVER	LYLE , JAMES D.	

	Issued			BEARING ORE AND METHOD OF MANUFACTURE	
06161629	Not Issued	164	06/26/1980	XEROGRAPHIC DEVELOPMENT	LYLES , JAMES M.

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PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = ALTMANN

First Name = WILLIAM

Application#	Patent#	Status	Date Filed	Title	Inventor Name 4
60271331	Not Issued	159	02/23/2001	CLOCK JITTER LIMITING SCHEME USING DIGITAL SCALING	ALTMANN, WILLIAM C.
09954663	Not Issued	030	09/12/2001	METHOD AND APPARATUS FOR SENDING AUXILIARY DATA ON A TMDS-LIKE LINK	ALTMANN, WILLIAM C.
09942885	Not Issued	041	08/29/2001	AUXILIARY DATA TRANSMITTED WITHIN A DISPLAY'S SERIALIZED DATA STREAM	ALTMANN, WILLIAM C.
09905318	Not Issued	041	07/13/2001	CLOCK JITTER LIMITING SCHEME IN VIDEO TRANSMISSION THROUGH MULTIPLE STAGES	ALTMANN, WILLIAM C.

Inventor Search Completed: No Records to Display.

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Last Name

altnmann

First Name

william

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